

# Signetics Bipolar & MOS Memory Data Manual By Signetics

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SIGNETICS Signetics Digital Linear MOS Data Book Signetics Corp, Sunnyvale, CA, 1974, 54/74; MSI/TTL 8000; Bipolar Memory; ECL 1,000/10,000; Linear; MOS; and D

## **Patent us5298803 - programmable logic device**

Programmable logic device having low power microcells with selectable registered and combinatorial output signals US 5298803 A. Signetics BIPOLAR and MOS Memory,

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Over two years ago we wrote about the history of the Signetics 8X300 Zilog, MOS all are familiar and external memory (and no real way to access data

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Bipolar transistors; Data converters; Diodes; ESD protection, TVS, filtering and signal conditioning; type number or part of a type number e.g. NXP Semiconductors

## **Patent us4422072 - field programmable logic array**

The Signetics 82S102/103 and 82S100/101 are described in Signetics Bipolar & MOS Memory Data Manual, citerade patent Registreringsdatum Publiceringsdatum S kande

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INTRODUCING THE 3000 . memory data in the specified register, SIGNETICS BIPOLAR FIELD-PROGRAMMABLE LOGIC ARRAY. 82S100, 82S101

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Signetics, once a major player in semiconductor manufacturing, NXP include integrated circuits, bipolar & MOS, Dolby circuit, logic, memory, analog circuits,

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Integrated Circuits Beginnings of MOS memory. Semiconductor memory development started with bipolar SRAM and the development of MOS SRAM started somewhat later.

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### **Ic announcements**

Bipolar MOS SOS COS/MOS COS/MOS CMOS Nov. 29 Schottky Data Device 1506 MD Scientific Signetics SN7---DM7577 TMS4050/60 TA6778 CD4000B SCL5533 82S114, 82S115

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82S115 Signetics Mos and Bipolar ROM / PROM 1975 82S116 Signetics Integrated Circuits Catalogue 1978/79 82S117 Signetics Integrated Circuits Catalogue 1978/79

**Patent us5331227 - programmable logic device**

Signetics, Bipolar and MOS Memory Data Manual Signetics Inc. pp. 156-165 Jan. 1979. 51 \* Teil et al., A Logic Minimizer for KLSI PLA Design ACM IEEE Ninteenth Design

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**Patent us5220215 - field programmable logic array**

A programmable logic device (PLD) is disclosed which can efficiently, in a real estate sense, emulate a Mealy state machine. Specifically, there is a PLD which has

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